1. Numbers of Hybrid SRCs, HRCs and PVSs (including re-buffering) have been redefined and agreed again, as follows:

**HD:** 10 SRCs, 16 HRCs, i.e. total of 160 PVSs

**VGA/WVGA:**VGA1: 15 SRCs, 16 HRCs, rebuffering, 90 PVSsVGA2: 10 SRCs, 16 HRCs , total of 160 PVSsVGA3: 16 SRCs, 19 HRCs, total of 160 PVSsWVGA1: 10 SRCs, 16 HRCs , total of 160 PVSs
WVGA2: 8 SRCs, 16 HRCs, rebuffering , 90 PVSs

1. Review of status of test designs has been made:
	1. HD:
		1. AGH - done, sent to Silvio, issues with interlaced play-out, issue postponed
		2. DT - done
		3. FUB (720p) - Vittorio absent, state unknown, rather not done
		4. Ghent - sent, done
		5. IRCCyN (DT) - done by Savvas
		6. Yonsei - done, not yet submitted, to be submitted by the end of this week
	2. VGA:
		1. OPTICOM (no re-buffering) - done
		2. SwissQual (re-buffering) – done (2nd HRC with rebuffering to be added)
		3. Yonsei - done
	3. WVGA:
		1. Acreo (re-buffering) - not yet done
		2. Yonsei – done
2. Chulhee's expectations to have everything ready, including data analysis, by the next meeting.
3. Time is needed to investigate re-buffering. Around a 1.5 month, until end of January. Acreo then will submit at least one test design. No matter if or without re-buffering.
4. Labs able to create re-buffering sought.
5. **Decision:** After test design is finalized, ILGs should generate one PVS per equipment setup. Proponents will check the model can read and process the pcap files.
6. **Decision:** All videos from the common set will have 14 seconds duration (this includes the rebuffering tests).
7. **Decision:** WVGA videos of the common set will be cropped to VGA for inclusion in VGA tests.
For cropped play-out, care must be taken to eliminate PVS sequences having distortions in cropped areas only.
8. No test design includes MPEG-TS/UDP/IP.
Note: Sirannon can handle this protocol.
**Vote**: Should MPEG-TS/UDP/IP be skipped in the evaluation phase.
\* 4 votes for skipping (OPTICOM, DT, ITS, SWISSQUAL).
\* No-one against.
**Decision:** MPEG-TS/UDP/IP will be excluded in the evaluation phase.
9. Schedule has been proposed by SwissQual and modified by the group. It is as follows:

**Schedule**:

1. Deadline for test design: end of December (except Acreo, FUB, finished by end of January).
For Acreo, ILG or Proponents (except SwissQual) explore to generate PVSs with rebuffering. If a proponent generates rebuffering PVSs for Acreo, the remaining PVSs without rebuffering should be done by ILGs.
If all these efforts fail, a conference call will be needed.
For the remaining test design (except FUB) comments should be received by the end of December. Otherwise, the test design will be considered final.
2. Joint effort: source scene pool finished by January 12
3. PVS creation and common set PVS creation: finished by end of February.
ILGs, who did not generate test vectors, but create PVSs for validation should submit one PVS per equipment setup.
4. Check and discuss PVSs (PVSs fall within calibration limits, bitstreams are compliant), finished March 15.
5. Objective score submission by end of April.
6. ILG checks objective scores.
7. Subjective testing, finisched by end of April. Data evaluation: until next VQEG meeting.
8. Verify that encrypted models do not use payload.

**Proposal by SwissQual**: In case of too much delay for test design or PVS creation for a dataset, we propose to **skip the data set**, to avoid additional delay.